PROPERTY

10

INPUT UNIT
11

EFFECTIVE TEST
PATTERN
CALCULATING UNIT
12

COVERAGE RATIO
CALCULATING UNIT
14

OUTPUT
UNIT
15

DISPLAY
UNIT
20

INPUT UNIT
13

TEST PATTERN

FIG.1

TITLE: APPARATUS AND METHOD FOR
        CALCULATING SIMULATION
        COVERAGE
INVENTORS: Kenji ABE, et al.
SERIAL NO.: Unassigned
DOCKET NO.: 1095.1281

	t			t+1		
	P	Q	EN	P	Q	EN
PATTERN α	1	1	0	0	0	0
PATTERN β	1	1	0	0	0	1
PATTERN γ	0	0	0	0	0	1
PATTERN δ	0	1	0	0	1	1
PATTERN ε	1	0	0	1	0	1

FIG.2

TITLE: APPARATUS AND METHOD FOR
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       COVERAGE
INVENTORS: Kenji ABE, et al.
SERIAL NO.: Unassigned
DOCKET NO.: 1095.1281

START

S1 — READ PROPERTY

S2 — CALCULATE EFFECTIVE
     TEST PATTERNS

S3 — READ TEST PATTERNS

S4 — CALCULATE
     COVERAGE RATIO

END

FIG.3

TITLE: APPARATUS AND METHOD FOR
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30

31   32   33   34

| CPU | ROM | RAM | HDD |

37

35   36

| INPUT I/F | | GRAPHIC I/F |

35b

35a   36a
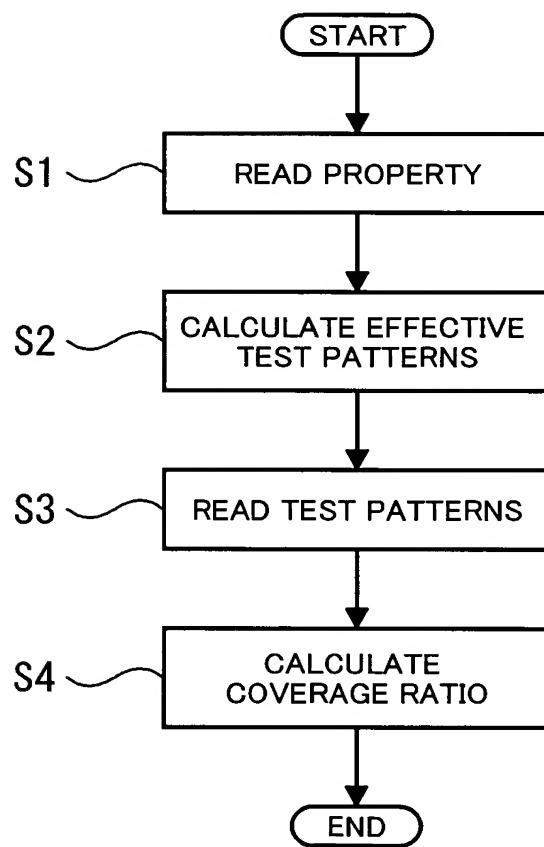
FIG.4

TITLE: APPARATUS AND METHOD FOR
CALCULATING SIMULATION
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INVENTORS: Kenji ABE, et al.
SERIAL NO.: Unassigned
DOCKET NO.: 1095.1281
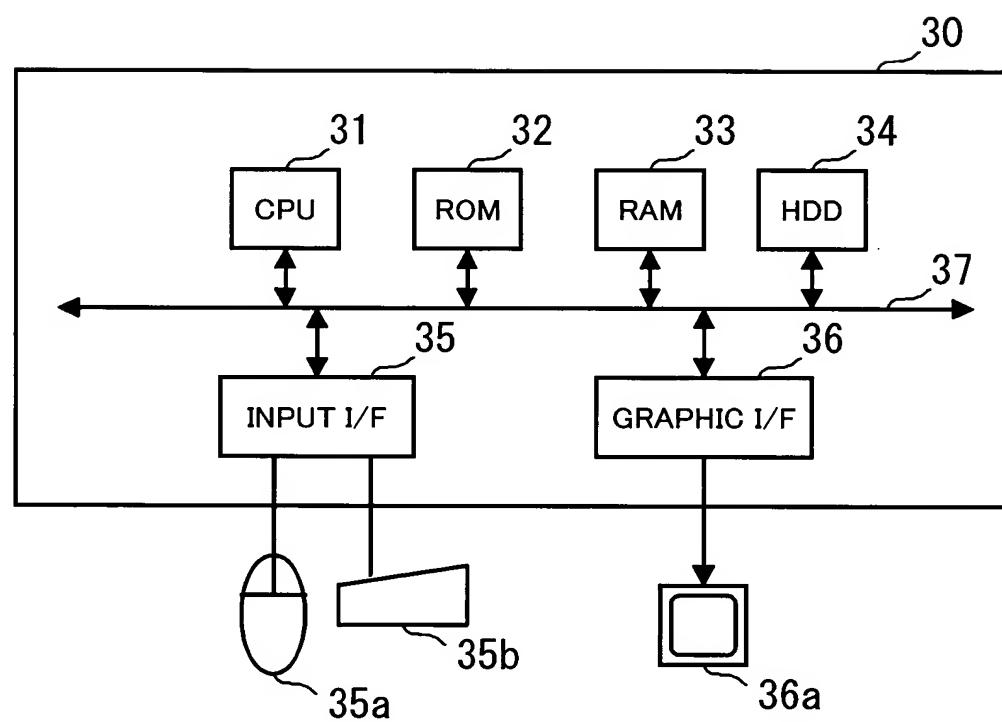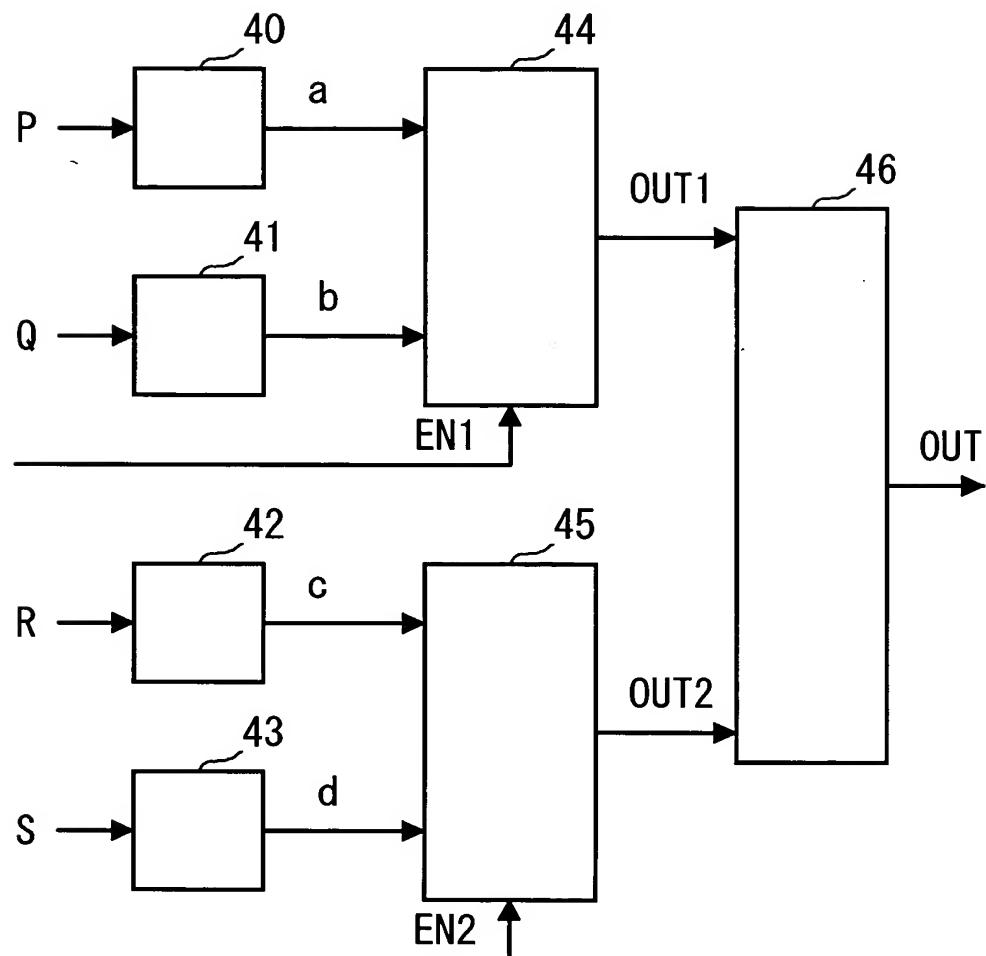
FIG.5

TITLE: APPARATUS AND METHOD FOR
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COVERAGE
INVENTORS: Kenji ABE, et al.
SERIAL NO.: Unassigned
DOCKET NO.: 1095.1281

	t				t+1				t+2			
	P	Q	R	S	P	Q	R	S	P	Q	R	S
PATTERN(1)	0	0	0	0	—	—	—	—	—	—	—	—
PATTERN(2)	0	0	0	1	—	—	—	—	—	—	—	—
PATTERN(3)	0	0	1	0	—	—	—	—	—	—	—	—
PATTERN(4)	0	0	1	1	—	—	—	—	—	—	—	—
PATTERN(5)	0	1	0	0	—	—	—	—	—	—	—	—
PATTERN(6)	0	1	0	1	—	—	—	—	—	—	—	—
PATTERN(7)	0	1	1	0	—	—	—	—	—	—	—	—
PATTERN(8)	0	1	1	1	—	—	—	—	—	—	—	—
PATTERN(9)	1	0	0	0	—	—	—	—	—	—	—	—
PATTERN(10)	1	0	0	1	—	—	—	—	—	—	—	—
PATTERN(11)	1	0	1	0	—	—	—	—	—	—	—	—
PATTERN(12)	1	0	1	1	—	—	—	—	—	—	—	—
PATTERN(13)	1	1	0	0	—	—	—	—	—	—	—	—
PATTERN(14)	1	1	0	1	—	—	—	—	—	—	—	—
PATTERN(15)	1	1	1	0	—	—	—	—	—	—	—	—
PATTERN(16)	1	1	1	1	—	—	—	—	—	—	—	—

FIG.6A

	t		t+1		t+2	
	EN1	EN2	EN1	EN2	EN1	EN2
PATTERN1	—	—	1	1	—	—

FIG.6B

	t		t+1		t+2	
	OUT1	OUT2	OUT1	OUT2	OUT1	OUT2
PATTERN①	—	—	—	—	0	1
PATTERN②	—	—	—	—	1	0
PATTERN③	—	—	—	—	1	1

FIG.6C

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A 〜
```
always @(posedge CLK) begin
    if(P)
        a <=a1;          (1)
    else
        a <=a2;          (2)
end
```

B 〜
```
always @(posedge CLK) begin
    if(Q)
        b <=b1;          (3)
    else
        b <=b2;          (4)
end
```

C 〜
```
always @(posedge CLK) begin
    if(EN)
        OUT <=a & b;     (5)
    else
        OUT <=0;         (6)
end
```

FIG.7
PRIOR ART

TITLE: APPARATUS AND METHOD FOR
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DOCKET NO.: 1095.1281
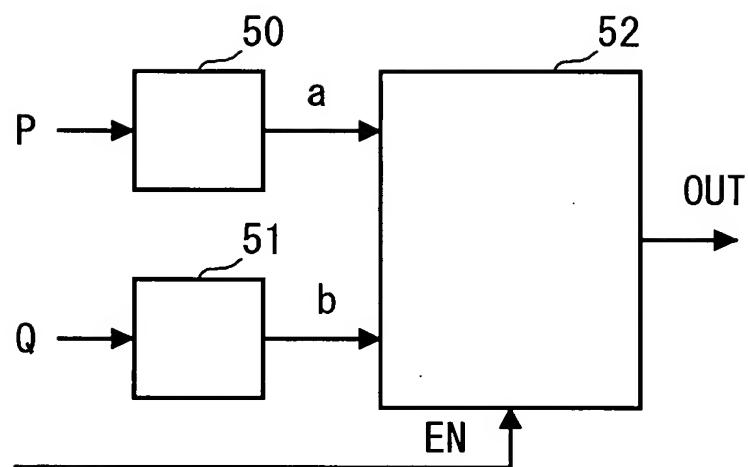
FIG.8
PRIOR ART

TITLE: APPARATUS AND METHOD FOR
         CALCULATING SIMULATION
         COVERAGE
INVENTORS: Kenji ABE, et al.
SERIAL NO.: Unassigned
DOCKET NO.: 1095.1281

	t			t+1		
	P	Q	EN	P	Q	EN
PATTERN(1)	0	0	—	—	—	1
PATTERN(2)	0	1	—	—	—	1
PATTERN(3)	1	0	—	—	—	1
PATTERN(4)	1	1	—	—	—	1

FIG.9
PRIOR ART

TITLE: APPARATUS AND METHOD FOR
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       COVERAGE
INVENTORS: Kenji ABE, et al.
SERIAL NO.: Unassigned
DOCKET NO.: 1095.1281

	t			t+1		
	P	Q	EN	P	Q	EN
PATTERN α	1	1	0	0	0	0
PATTERN β	1	1	0	0	0	1

FIG.10
PRIOR ART